App1. No. 09/4 ,370

Amdt. dated January 13, 2004

Reply to Office Action of December 3, 2003

This listing of claims replaces all prior versions, and listings of claims in the instant application:

Listing of Claims:

(Original) A data structure comprising:

a thread control block, wherein said thread control block is described by a first data structure; and a message, wherein said message is described by a second data structure and said first data structure comprises said second data structure.

- 2. (Original) The data structure of claim 1, wherein said first data structure is configured to store information used to control execution of a thread.
- 3. (Original) The data structure of claim 1, wherein said second data structure is configured to store a message.
- 4. (Original) The data structure of claim 1, wherein said first data structure further comprises:

a process control block pointer, wherein said process control block pointer points to a process control block; processor information; and stack information.

5. (Original) The data structure of claim 4, wherein said process control block comprises:

memory information;
thread information;
device driver information; and
stack information.

- 6. (Original) The data structure of claim 4, wherein said processor information comprises:
 - a processor identifier; and

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thread information.

7. (Original) The data structure of claim 1, wherein said second data structure further comprises: control information.

8. (Original) The data structure of claim 7, wherein said second data structure further comprises:

data.

Claims 9 to 58 (Cancelled)

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